

Synchronous Communications Product Line

ICs for Data Communications

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Device	Technology Description	Package Type(s)	I _{cc} mA @ V _{cc} = 5V	Temp Range deg C	Speed	Key Features
SCN2651	NMOS Programmable Communication Interface (PCI)	PDIP28 PLCC28	150	0 to +70 -40 to +85	0-1Mbps data rate 16 internal baud rates (50 to 19.2K) external BRG	<ul style="list-style-type: none"> Synchronous/asynchronous operation (full/half duplex) 5-8 bit character (1, 1.5, 2 stop bits in asynchronous mode) Odd, even, no parity in asynchronous mode Single or dual SYN, transparent and non-transparent auto SYN/DLE insertion/deletion in synchronous operations Double buffer transmit and receive operation
SCN2661 SCN68661	NMOS Enhanced Programmable Communication Interface (PCI)	PDIP28 PLCC28	150	0 to +70 -40 to +85	19 internal baud rates (50 to 38.4K)	<ul style="list-style-type: none"> Functionally it is an enhanced version of SCN2651 Available in three different baud rate versions Enhanced for BREAK detect and external jam sync, DLE detect, SYN1 stripping, drop RTS, stop bit search and data bus timing/drivers
SCN2652 SCN68652	NMOS Enhanced Programmable Communication Interface (PCI)	PDIP40 PLCC44	150	0 to +70 -40 to +85	0-2Mbps data rate	<ul style="list-style-type: none"> Supports synchronous Bit Oriented (SDLC, ADCCP, HDLC) and Character Oriented (DDCMP, DISYNC) Protocols SYNC Generation, detection in BCP and Auto Zero insertion/deletion in BOP modes Programmable 8 or 16 bit data bus Full/half duplex transmit and receive operation
SCC26562 SCN68562	NMOS DUAL Universal Serial Communication Controller (DUSCC)	PDIP48 PLCC52	275	0 to +70	0-4Mbps data rate 16 internal baud rates (50 to 38.4K) internal DPLL and counters	<ul style="list-style-type: none"> Dual channel synchronous and asynchronous operation Bit Oriented (HDLC, SDLC, X.25/75) and Character Oriented (BISYNC, DDCMP) Protocols Parity and Frame Check Sequence (FCS) generation/checking Poll, Interrupt, Vectored Interrupt, Modified Vector Interrupt, DMA and Wait Data Transfer modes Data coding/encoding modes: NRZ, NRZI, FMO, FM1, Manchester On-chip oscillator (16MHz), 16 bit counter/timer, DPLL Modern control inputs/outputs and external synchronous pin 4 byte receiver and transmitter FIFOs and Rx/Tx shift registers for each channel Rx overrun and Tx underrun controls Full/half duplex transmit and receive operation
SC26C562 SC68C562	CMOS DUAL Universal Serial Communication Controller (CDUSCC)	PDIP48 PLCC52	80	PDIP48 0 to +70 PLCC52 0 to +70 -40 to +85	0-1Mbps data rate 19 internal baud rates (50 to 64K) internal DPLL and counters	<ul style="list-style-type: none"> Functionally it is an enhancement of SCN26562/68562 Faster transmit/receive data rate (10MHz) 16 byte deep Tx/Rx FIFOs 170ns bus cycle More and higher internal BRG selections Better control over individual interrupt conditions Supports X.21 pattern recognition Provides more Tx/Rx status information